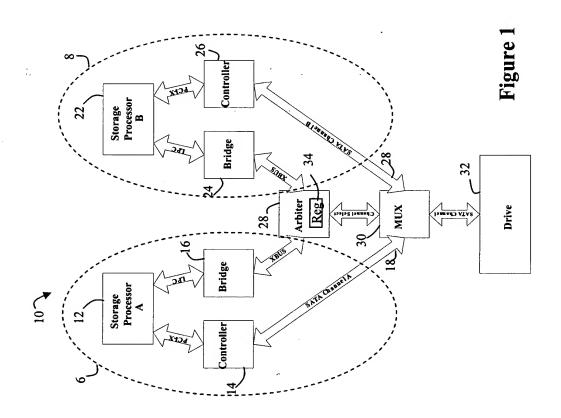
Date f Deposit: December 12, 2003
Autorney Docket: 130-020
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32 20 D0 req reg req req mux_x_ovrd mux_x_ovrd 34 peer_req peer_req DI D 36 D2 D2 Grant Grant N/A N/A m_reset_n m_reset_n m_reset_n m_reset_n 38 D3 D3 27 17 Bit Name (Write) Bit Name (Write) Bit Name (Read) Bit Name (Read) Bit# Bit#

34 Mux Control Registers

Figure 2

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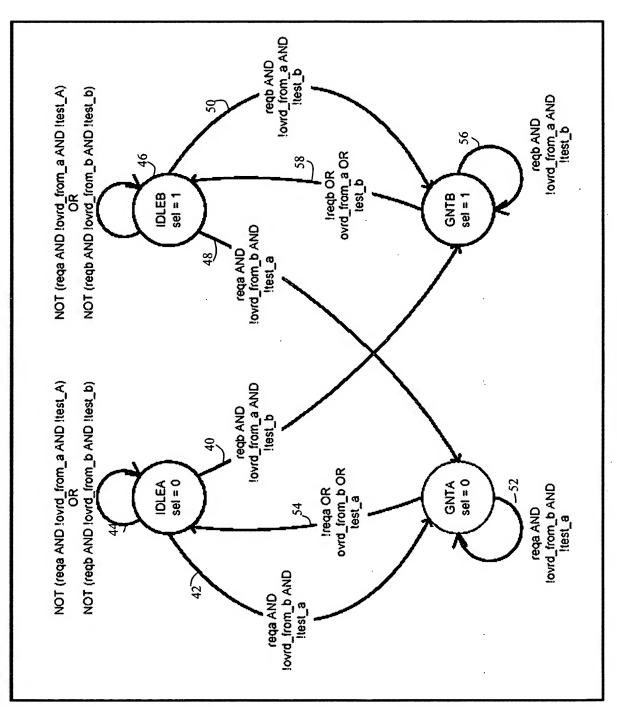


Figure 3